

PM202 SPECIFICATIONS

Table 6 contains the environmental specifications for the PM202. Note that the environmental specifications for the PM202 are more restrictive than those for the 1240 Logic Analyzer itself.

The specifications shown in Table 7 refer to a 1240 Logic Analyzer system which includes a PM202 Personality Module.

Table 6
PM202 ENVIRONMENTAL SPECS

| Characteristic | Description |
|---|----------------------------|
| Temperature Operating Storage | 0°C - 50°C -55°C - 75°C |
| Humidity | 0% - 90% |
| Altitude (max.) Operating Storage | 15,000 feet 50,000 feet |

Table 7
PM202 - 1240 SYSTEM SPECS

| Characteristic | Performance Requirement | Supplemental Information |
|--|--------------------------|--|
| CLK OUT Frequency | 10 MHz max. | |
| Data Setup Time | 30 ns min. | Measured from $\overline{\text{J}} \overline{\text{DEN}}$ |
| Data Hold Time | 0 ns max. | Measured from $\overline{\text{J}} \overline{\text{DEN}}$ |
| Delays* | | |
| ALE $\overline{\text{J}}$ | 32 ns min. 85 ns max. | Calculated from $\overline{\text{J}}$ CLKOUT prior to T1 (TCHLH) |
| ALE $\overline{\text{L}}$ | 39 ns max. 25 ns min. | Measured from $\overline{\text{J}}$ CLKOUT in T1 (TCHLL) |
| $\overline{\text{WR}}$ $\overline{\text{J}}$ | 55 ns max. 34 ns min. | Measured from $\overline{\text{L}}$ CLKOUT in T4 (TCVCTX) |
| $\overline{\text{WR}}$ $\overline{\text{L}}$ | 70 ns max. 27 ns min. | Measured from $\overline{\text{L}}$ CLKOUT in T2 (TCVCTV) |
| $\overline{\text{RD}}$ $\overline{\text{J}}$ | 55 ns max. 34 ns min. | Measured from $\overline{\text{L}}$ CLKOUT in T4 (TCLRH) |
| $\overline{\text{RD}}$ $\overline{\text{L}}$ | 70 ns max. 27 ns min. | Measured from $\overline{\text{L}}$ CLKOUT in T2 (TCLRL) |
| QS0 | 14 ns max. | Input to output transit time |
| QS1 | 36 ns max. | Input to output transit time |
| PM202 Output Levels | | |
| Logical Lows | 0.8 V max. | |
| Logical Highs | 2.4 V min. | |
| Input Capacitance | | 30 pF nominal |
| Max. Non-Destructive Input Voltage | | -2 V to +7 V |

* When the 80188 is installed in the PM202 flex-circuit, it always runs in the Queue Status mode. If the PM202 jumpers are configured for QSMO, the PM202 returns the 80188's queue status signals to the system under test. If the PM202 jumpers are configured for Not Queue Status Mode, the PM202 provides the system under test with synthesized ALE, $\overline{\text{RD}}$, and $\overline{\text{WR}}$ signals.

REPLACEABLE PARTS LIST

80188 MNEMONICS ROM PACK — 12RM06

| NUMBER | TEK. P/N | DESCRIPTION |
|--|-------------|--|
| ELECTRICAL (REFER TO SCHEMATIC IN 1240 SERVICE MANUAL) | | |
| A43 | 670-8172-00 | CRT. BOARD ASSY: 32/64K MEMORY ROM PACK (U200, U300 EPROMs ARE NOT PART OF A43) |
| A43C100 | 281-0775-00 | CAP, FIXED, CER, DI: 0.1 uF, 20%, 50V |
| A43C400 | 281-0775-00 | CAP, FIXED, CER, DI: 0.1 uF, 20%, 50V |
| CHASSIS PARTS | | |
| U200 | 160-2466-00 | MICROCKT, DGTL: 16384 x 8 EPROM, PRGM |
| U300 | 160-2465-00 | MICROCKT, DGTL: 16384 x 8 EPROM, PRGM |
| MECHANICAL (REFER TO EXPLODED VIEW DRAWING) | | |
| 1 | 334-0174-00 | 1 MARKER, IDENT: MKD 80188 ROM PACK |
| 2 | 200-2503-01 | 1 COVER, ROM PACK: TOP (ATTACHING PARTS) |
| 3 | 211-0012-00 | 4 SCREW, MACHINE: 4.40 x 0.375, PHD, STL — — * — — |
| 4 | - - - - - | CKT BOARD ASSY: 32/64K MEMORY ROM PACK (SEE A43 REPL) |
| 5 | 131-0993-00 | 2 • BUS CONDUCTOR: 2 WIRE, BLACK |
| 6 | 131-0608-00 | 6 • TERMINAL, PIN: 0.365 L x 0.025 PH BRZ GOLD |
| 7 | 136-0755-00 | 2 • SKT, PL-IN ELEC: MICROCIRCUIT, 28 DIP |
| 8 | 337-3122-00 | 1 SHIELD, ELEC: STATIC |
| 9 | 200-2504-01 | 1 COVER, ROM PACK: BOTTOM |
| 10 | 334-4727-00 | 1 MARKER, IDENT: MKD PROM PROGRAM IDENT |
| STANDARD ACCESSORIES | | |
| | 070-4845-00 | MANUAL, TECH: INSTRUCTION |